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On the parallelization of boundary element codes using standard and **portable** libraries

MT Cunha, JCF Telles, A Coutinho, J Panetta - Engineering analysis with ..., 2004 - Elsevier

... Parallel programming concepts are applied here to create a **portable** shared memory implementation of the **code**. ... This **flag** has its greatest effects on loop intensive **code** and math intensive **code** by enabling **software** pipelining and loop nest optimizations. ...

[Cited by 13](#) - [Related articles](#) - [All 4 versions](#)SPEComp: A new benchmark suite for measuring parallel computer performance[\[PDF\] from psu.edu](#)

V Aslot, M Domeika, R Eigenmann, G Gaertner, ... - OpenMP Shared ..., 2001 - Springer

... to the SPEchpc suite, we wanted to create smaller, more easily **portable** and executable ... separate compiler **flags** may be used for each program, and limited source **code** modifications, restricted ... portability prob- lems, defining new data sets, creating self-validation **code** for each ...

[Cited by 130](#) - [Related articles](#) - [BL Direct](#) - [All 13 versions](#)Implementation of a **portable** Fortran 77 compiler using modern tools

SI Feldman - ACM SIGPLAN Notices, 1979 - portal.acm.org

... The VAX version was producing correct **code** after two days' work. ... UNIX system was quite young when Fortran was installed and there were a number of anomalies in other **software**.) The Fortran ... Making the Fortran compiler **portable** and able to cross-compile did not cost much. ...

[Cited by 26](#) - [Related articles](#) - [All 3 versions](#)[\[PDF\] The NAS parallel benchmarks 2.0](#)[\[PDF\] from psu.edu](#)

D Bailey, T Harris, W Saphir, R Van Der Wijngaart, A ... - 1995 - Citeseer

... report the highest level of **optimization** used, and which routines were not compiled with this level of **optimization**.) NAS will ... NAS encourages vendors to release source **code** as soon as possible, however. ... NPB 2.0 is the first release of source "**portable**" NAS Parallel benchmarks ...

[Cited by 624](#) - [Related articles](#) - [View as HTML](#) - [All 16 versions](#)The Nestor library: A tool for implementing fortran **source** to **source** transformations[\[PDF\] from psu.edu](#)

GA Silber, A Darté - High-Performance Computing and Networking, 1999 - Springer

... **code** transformations such as loop transfor- mations, scalar expansion, array privatization, dead **code** removal, etc. ... For this reason, we decided to rely on a free **software** tool, named Petit [18 ... This strategy is not only simpler to implement, it is also more **portable**: potential bugs in ...

[Cited by 13](#) - [Related articles](#) - [BL Direct](#) - [All 9 versions](#)

A **code** generation interface for ANSI CCW Fraser, DR Hanson - **Software: Practice and Experience**, 1991 - Wiley Online Library

... Suite for ANSI C. The production versions of lcc use the interface described here, but use back ends in which instruction selection and **optimization** are generated ... The name of most variables is the name used in the source **code**. ... global emits **code** to define a global variable. ...

Cited by 92 - [Related articles](#) - [All 24 versions](#)Attributed linear intermediate representations for retargetable **code** generatorsM Ganapathi, CN Fischer - **Software: Practice and ...**, 1984 - interscience.wiley.com

... to a new machine or using the same **code** generator for a new source language. ... Whereas Janus was developed to study **portable** compilers, TCOL is more ambitious in using the ... for storage binding (eg type, size and scope of a variable) and **code optimization** (eg execution ...

Cited by 22 - [Related articles](#) - [All 4 versions](#)Automatic generation of OpenMP **directives** and its application to computational fluid dynamics codes[\[PDF\] from psu.edu](#)H Jin, M Frumkin, J Yan - **High Performance Computing**, 2000 - Springer

... The generated parallel codes contain **portable** interface to message passing standards, such as MPI and PVM, through a low-overhead library. ... The generated parallel **code** is produced using a source-to-source transformation with very little modification to the original ...

Cited by 29 - [Related articles](#) - [BL Direct](#) - [All 13 versions](#)Comparison of different computer platforms for running the versatile advection **code**[\[PDF\] from psu.edu](#)G Tóth, R Keppens - **High-Performance Computing and Networking**, 1998 - Springer

... In the source **code**, we simply used uppercase letters in the declaration of the arrays that should not be distributed by f90tohp. ... 5 Conclusions In summary, the Versatile Advection **Code** fulfills its original design specifica- tions by being versatile, **portable**, and efficient on ...

Cited by 5 - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)Identifying and exploiting spatial regularity in data memory references[\[PDF\] from psu.edu](#)T Mohan, BR Supinski, SA McKee, F ... - **Proceedings of the ...**, 2003 - portal.acm.org

... PAPI [24] is a **portable** API to access these counters. ... Occasionally, however, a source modification or compiler di- rective is necessary to enable the application of certain ... For instance, tiling optimizations require a loop interchange, which may be illegal until the **code** is suitably ...

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